

EBSD Based Analysis of Lead Free Solders*

Introduction

One of the challenges of the electronic packaging industry is to move from traditional lead based solders to lead free solders in order to reduce the use of toxic materials. However, cracking of the solder joints is one of the obstacles of moving to lead free solders. Current characterization tools being used to solve this problem are basic Scanning Electron Microscope (SEM) imaging and Energy Dispersive Spectroscopy (EDS).

Electron Backscatter Diffraction (EBSD) offers new possibilities for characterizing cracking in solders. Often, SEM imaging suggests that the cracks are intergranular – that is, they propagate at grain boundaries. However, this is not always obvious from SEM images alone. EBSD can unambiguously identify cracks as intergranular or transgranular. In addition, statistical analysis of grain boundaries can identify the types of grain boundaries that may be susceptible or resistant to cracking. EBSD is also capable of identifying areas of high local orientation variations, which are indicative of the buildup of localized residual strain. If the stress state can be accurately determined then the anisotropy of elastic stiffness or incompatibility at grain boundaries can also be modeled. Such analyses done over a matrix of specimens can help identify potential process improvement opportunities.

Sample Preparation

EBSD is a surface sensitive technique, sampling only 10 to 20 nm into the depth of the sample. Thus, in order to get good EBSD data it is imperative to have a well prepared sample surface. Sample preparation of lead free solders can be difficult for EBSD due to the softness of the solder, the relative hardness differences between the solder alloy, the contact metal layers, and the intermetallic phases present. Often, polishing times and forces will need to be reduced for optimal results. In addition, polishing to the correct plane within the sample can be difficult. A procedure recommended by Allied High Tech Products, Inc. is listed in Table 1. As different regions of the sample surface may be non-conductive, it is important to create an electric grounding pathway to the analysis surface for stable and distortion-free EBSD mapping. One approach is to sputter deposit a thin (15-30 Å) conductive carbon layer over the sample surface. However, careful control of the film thickness is required to minimize EBSD pattern degradation. Another approach is to use a conductive paint (carbon or silver) to cover most of the sample surface area except for the region of interest. This can be accompanied by a thinner (5-10 Å) carbon coat over the analysis area. A third approach is to add conductive filler to the epoxy; however this can make sectioning to the desired plane more difficult. A fourth option, if applicable, is to use the low vacuum analysis capability available on many modern SEMs.

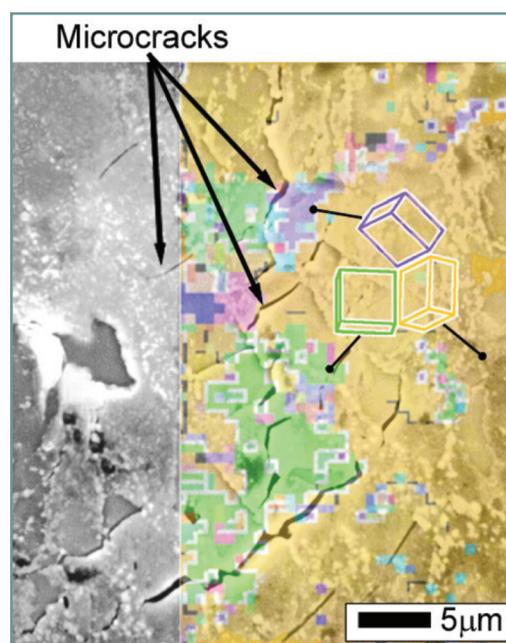


Figure 1. Micrograph of a lead free solder partially overlaid with an orientation map. Colored tetragons show the orientation of the colored regions in the map. (Courtesy of T. Bieler – Michigan State University)

*Extracted from an application note by T.R. Bieler, B.C. Ng, A.U. Telang and M.A. Crimp, Michigan State University, East Lansing, MI 48824 with additional work by Matt Nowell - EDAX

Step	1	2	3	4	5	6
Abrasive	320 Grit SiC Grinding Paper	600 Grit SiC Grinding Paper	6 μm Polycrystalline Diamond Suspension	3 μm Polycrystalline Diamond Suspension	1 μm Polycrystalline Diamond Suspension	0.05 μm Colloidal Silica
Polishing Cloth	NA	NA	Gold Label	White Label	Vel Cloth	ChemPol
Coolant	Water	Water	GreenLube	GreenLube	GreenLube	Water
Pressure	10 lb/F	10 lb/F	8 lb/F	8 lb/F	8 lb/F	5 lb/F
Time	2:00	2:00	6:00	3:30	3:30	2:00
Platen Speed	250 RPM Complementary	250 RPM Complementary	150 RPM Complementary	150 RPM Complementary	150 RPM Complementary	150 RPM Complementary
Polishing Head Speed	150 RPM	150 RPM	150 RPM	150 RPM	150 RPM	150 RPM

Table 1. Sample Preparation Procedure for Lead Free Solders (Allied High-Tech Products, Inc.)

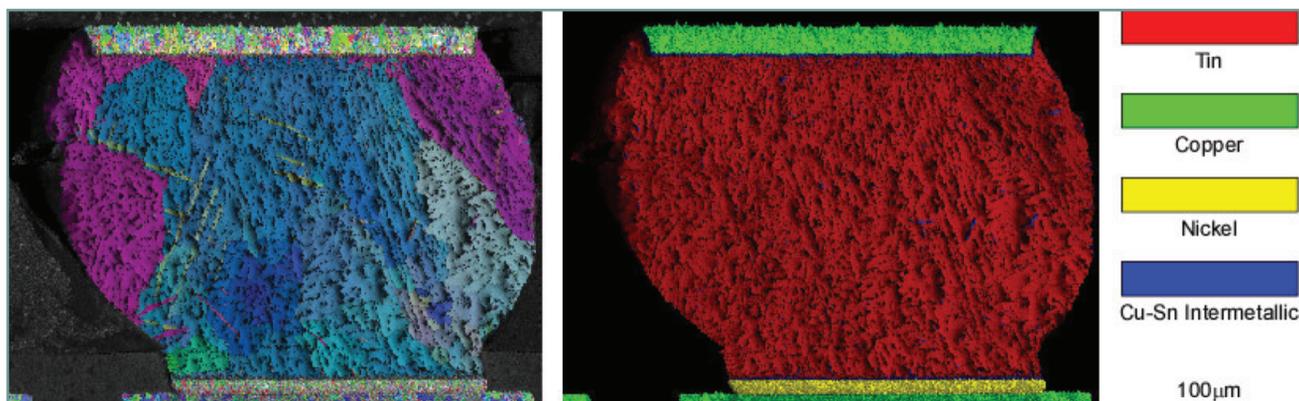


Figure 2. Orientation map and phase map from a lead-free solder bump.

Grain Boundary Sliding in a Lead Free Solder Joint

Orientation Imaging Microscopy (OIM™) has been used to study the mechanisms leading to cracking in lead free solder joints. OIM™ measurements of local crystal orientations help illuminate the process of damage nucleation. Lead free solders have complex slip system behavior due to their inherent tetragonal crystal structure. For example, in low strain rate creep/thermal cycling experiments on tin based solder joints (Figure 3), grain boundary sliding on low angle boundaries (shown in Figure 4) resulted in minimal crystal rotations,

whereas deformation at higher rates resulted in polyslip conditions that caused significant and predictable rotations using polycrystal plasticity models.

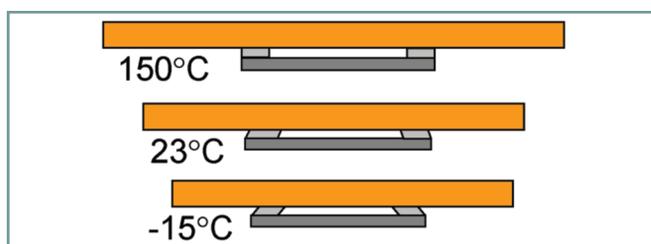


Figure 3. Schematic diagram of thermal cycling strains due to differential expansion between a copper substrate and a nickel simulated surface mount component. 4 hr. cycle, 20 min. 150°C, 3.5 hr. -15°C.

In Sn-Ag solder, most joints are nearly single crystals, resulting from the fact that crystal nucleation is very difficult due to the high enthalpy of fusion of tin. In some systems such as the one shown schematically in Figure 3, thermal cycling causes differential strains between the copper substrate and a surface mount component. In the solder joint, the differential strains have led to sliding phenomena at low angle boundaries, as illustrated in Figure 4 for the left joint. These boundaries were found to be low angle coincident site lattice boundaries. This is contrary to conventional wisdom that such boundaries would be resistant to grain boundary sliding. With thermal cycling, the initially strong single orientation was broken up into subgrains, weakening the initial texture, as is evident in comparing both the discrete and density pole figures in 4(a) and 4(b). The OIM™ orientation data can be coupled with a Finite-Element Model (FEM) to calculate Schmid Factors. Schmid factors give

an indication of a grain's propensity to slip, given the orientation of slip planes in the grain (as can be determined from the OIM™ data) and the local stress state (as computed by FEM). This information can be used to visualize slip systems and their relationship to grain boundary sliding. The ellipses on the right side of Figure 4(b) represent unit circles tilted on the plane indicated (the major axis is the plane trace). Slip directions with high Schmid factors are indicated. Sliding correlates closely with the highly stressed slip systems.

Conclusion

With proper sample preparation, OIM™ can be used to characterize the mechanisms that lead to cracking in lead free solder joints. The goal is to correlate these observations with parametric studies to improve the cracking resistance of lead free solder joints.

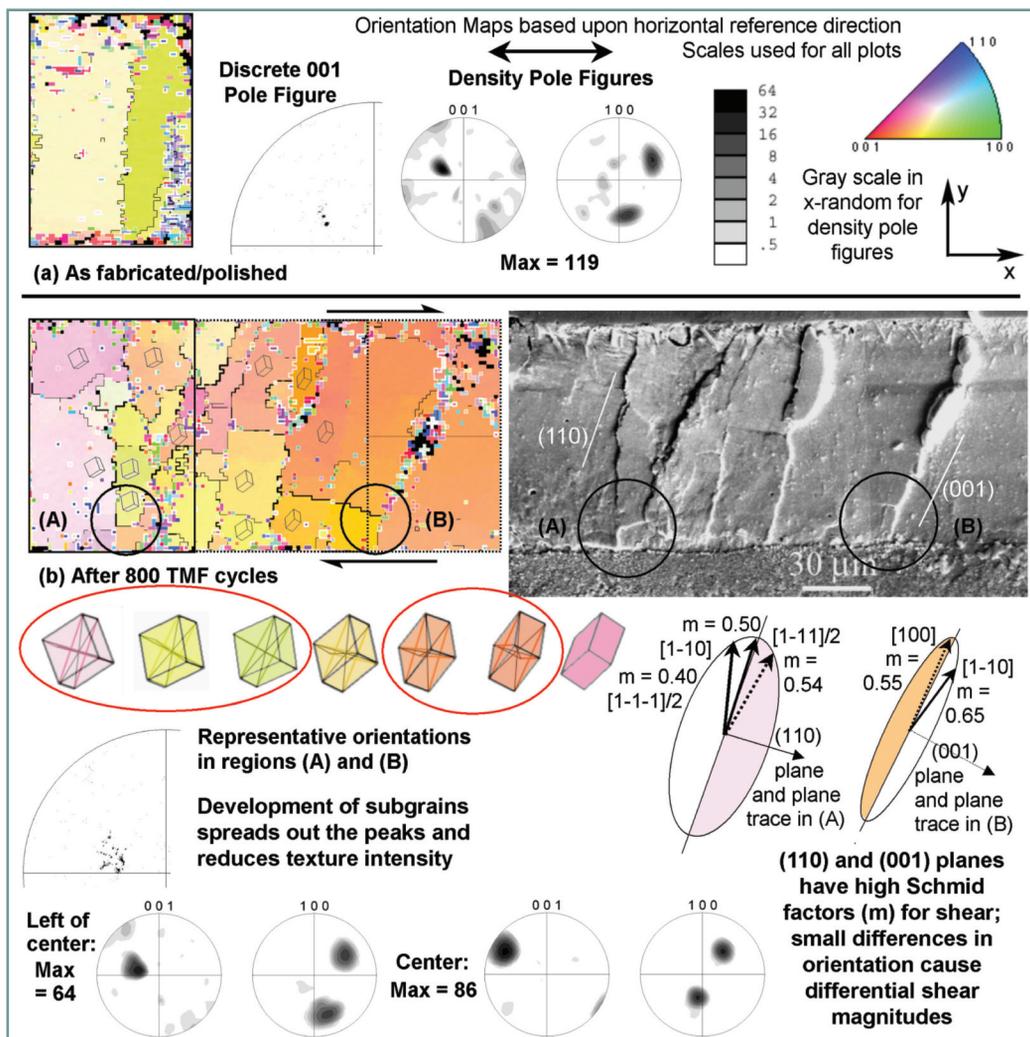


Figure 4. Left joint of thermomechanically fatigued solder joint specimen, (a) orientation map of as-fabricated initially polished surface of specimen, (b) after 800 TMF cycles between -15 °C and 150 °C.

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